

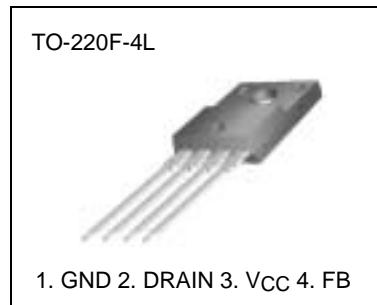
# KA5H0380R/KA5M0380R/KA5L0380R SPS

## Features

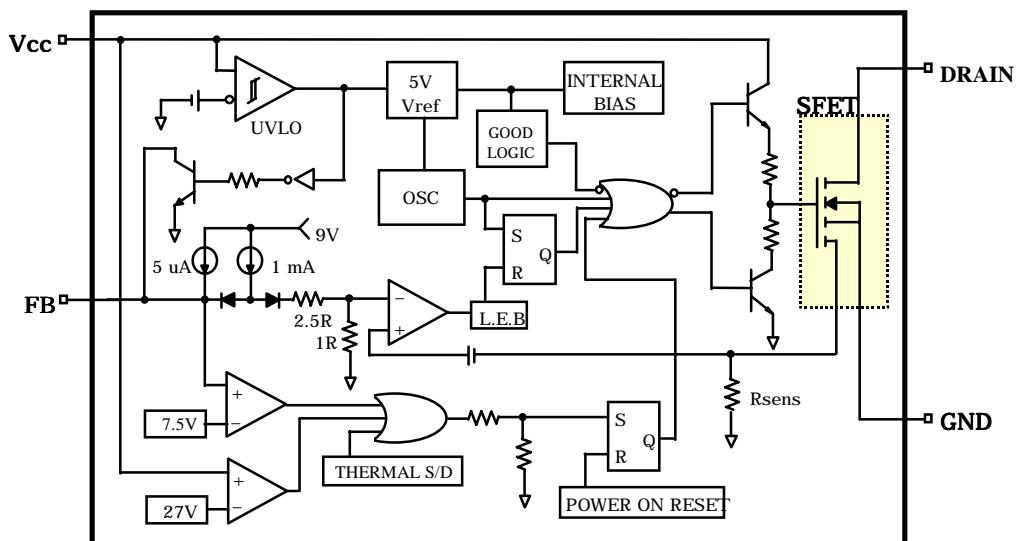
- Precision fixed operating frequency (100/67/50kHz)
- Low start-up current(typ. 100uA)
- Pulse by pulse current limiting
- Over current protection
- Over voltage protection (Min. 25V)
- Internal thermal shutdown function
- Under voltage lockout
- Internal high voltage sense FET
- Auto-restart mode

## Description

The SPS product family is specially designed for an off-line SMPS with minimal external components. The SPS consist of high voltage power SenseFET and current mode PWM IC. Included PWM controller features integrated fixed frequency oscillator, under voltage lock-out, leading edge blanking, optimized gate turn-on/turn-off driver, thermal shutdown protection, over voltage protection, and temperature compensated precision current sources for loopcompensation and fault protection circuitry. Compared to discrete MOSFET and PWM controller or RCC solution, a SPS can reduce total component count, design size, weight and at the same time increase efficiency, productivity, and system reliability. It has a basic platform well suited for cost-effective design in either a flyback converter or a forward converter.



## Internal Block Diagram



## Absolute Maximum Ratings

| Characteristic                                       | Symbol        | Value       | Unit                |
|--|---------------|-------------|---------------------|
| Drain-source (GND) voltage <sup>(1)</sup>            | VDSS          | 800         | V                   |
| Drain-Gate voltage ( $R_{GS}=1\text{M}\Omega$ )      | VDGR          | 800         | V                   |
| Gate-source (GND) voltage                            | VGS           | $\pm 30$    | V                   |
| Drain current pulsed <sup>(2)</sup>                  | IDM           | 12          | ADC                 |
| Single pulsed avalanche energy <sup>(3)</sup>        | EAS           | 95          | mJ                  |
| Avalanche current <sup>(4)</sup>                     | IAS           | 10          | A                   |
| Continuous drain current ( $T_C=25^\circ\text{C}$ )  | ID            | 3.0         | ADC                 |
| Continuous drain current ( $T_C=100^\circ\text{C}$ ) | ID            | 2.1         | ADC                 |
| Supply voltage                                       | VCC           | 30          | V                   |
| Analog input voltage range                           | VFB           | -0.3 to VSD | V                   |
| Total power dissipation                              | PD (watt H/S) | 35          | W                   |
|  | Derating      | 0.28        | W/ $^\circ\text{C}$ |
| Operating temperature                                | TOPR          | -25 to +85  | $^\circ\text{C}$    |
| Storage temperature                                  | TSTG          | -55 to +150 | $^\circ\text{C}$    |

**Notes:**

1.  $T_j=25^\circ\text{C}$  to  $150^\circ\text{C}$
2. Repetitive rating: Pulse width limited by maximum junction temperature
3.  $L=51\text{mH}$ , starting  $T_j=25^\circ\text{C}$
4.  $L=13\mu\text{H}$ , starting  $T_j=25^\circ\text{C}$

## Electrical Characteristics (SFET part)

(Ta = 25°C unless otherwise specified)

| Characteristic                                      | Symbol  | Test condition  | Min. | Typ. | Max. | Unit |
|---|---------|---|------|------|------|------|
| Drain-source breakdown voltage                      | BVDSS   | VGS=0V, ID=50µA   | 800  | —    | —    | V    |
| Zero gate voltage drain current                     | IDSS    | VDS=Max., Rating, VGS=0V  | —    | —    | 250  | µA   |
|   |         | VDS=0.8Max., Rating, VGS=0V, TC=125°C   | —    | —    | 1000 | µA   |
| Static drain-source on resistance <sup>(note)</sup> | RDS(ON) | VGS=10V, ID=0.5A  | —    | 4    | 5    | Ω    |
| Forward transconductance <sup>(note)</sup>          | gfs     | VDS=50V, ID=0.5A  | 1.5  | 2.5  | —    | S    |
| Input capacitance                                   | Ciss    | VGS=0V, VDS=25V, f=1MHz   | —    | 779  | —    | pF   |
| Output capacitance                                  | Coss    |   | —    | 75.6 | —    |      |
| Reverse transfer capacitance                        | Crss    |   | —    | 24.9 | —    |      |
| Turn on delay time                                  | td(on)  | VDD=0.5BVDSS, ID=1.0A<br>(MOSFET switching time are essentially independent of operating temperature)       | —    | 40   | —    | nS   |
| Rise time   | tr      |   | —    | 95   | —    |      |
| Turn off delay time                                 | td(off) |   | —    | 150  | —    |      |
| Fall time   | tf      |   | —    | 60   | —    |      |
| Total gate charge (gate-source+gate-drain)          | Qg      | VGS=10V, ID=1.0A, VDS=0.5BVDSS (MOSFET switching time are essentially independent of operating temperature) | —    | —    | 34   | nC   |
| Gate-source charge                                  | Qgs     |   | —    | 7.2  | —    |      |
| Gate-drain (Miller) charge                          | Qgd     |   | —    | 12.1 | —    |      |

**Note:**

Pulse test: Pulse width ≤ 300µS, duty ≤ 2%

$$S = \frac{1}{R}$$

## Electrical Characteristics (SFET part) (Continued)

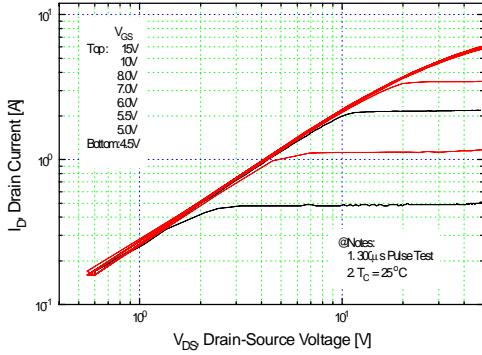
(Ta = 25°C unless otherwise specified)

| Characteristic  | Symbol              | Test condition                               | Min. | Typ. | Max. | Unit  |
|---|---------------------|--|------|------|------|-------|
| <b>REFERENCE SECTION</b>                                      |                     |  |      |      |      |       |
| Output voltage <sup>(1)</sup>                                 | Vref                | Ta=25°C                                      | 4.80 | 5.00 | 5.20 | V     |
| Temperature Stability <sup>(1)(2)</sup>                       | Vref/ΔT             | -25°C≤Ta≤+85°C                               | -    | 0.3  | 0.6  | mV/°C |
| <b>OSCILLATOR SECTION</b>                                     |                     |  |      |      |      |       |
| Initial accuracy  | FOSC                | <b>KA5H0380R</b>                             | 90   | 100  | 110  | kHz   |
| Initial accuracy  | FOSC                | <b>KA5M0380R</b>                             | 61   | 67   | 73   | kHz   |
| Initial accuracy  | FOSC                | <b>KA5L0380R</b>                             | 45   | 50   | 55   | kHz   |
| Frequency change with temperature <sup>(2)</sup>              |                     | -25°C≤Ta≤+85°C                               | -    | ±5   | ±10  | %     |
| <b>PWM SECTION</b>  |                     |  |      |      |      |       |
| Maximum duty cycle  | Dmax                | <b>KA5H0380R</b>                             | 62   | 67   | 72   | %     |
| Maximum duty cycle  | Dmax                | <b>KA5M0380R</b><br><b>KA5L0380R</b>         | 72   | 77   | 82   | %     |
| <b>FEEDBACK SECTION</b>                                       |                     |  |      |      |      |       |
| Feedback source current                                       | I <sub>FB</sub>     | Ta=25°C, 0V≤V <sub>fb</sub> ≤3V              | 0.7  | 0.9  | 1.1  | mA    |
| Shutdown delay current  | I <sub>delay</sub>  | Ta=25°C, 5V≤V <sub>fb</sub> ≤V <sub>SD</sub> | 4    | 5    | 6    | μA    |
| <b>OVER CURRENT PROTECTION SECTION</b>                        |                     |  |      |      |      |       |
| Over current protection                                       | I <sub>L(max)</sub> | Max. inductor current                        | 1.89 | 2.15 | 2.41 | A     |
| <b>UVLO SECTION</b>   |                     |  |      |      |      |       |
| Start threshold voltage                                       | V <sub>th(H)</sub>  | -  | 8.4  | 9    | 9.6  | V     |
| Minimum operating voltage                                     | V <sub>th(L)</sub>  | After turn on                                | 14   | 15   | 16   | V     |
| <b>TOTAL STANDBY CURRENT SECTION</b>                          |                     |  |      |      |      |       |
| Start current   | I <sub>ST</sub>     | V <sub>CC</sub> =14V                         | -    | 0.1  | 0.17 | mA    |
| Operating supply current<br>(control part only)               | I <sub>OPR</sub>    | V <sub>CC</sub> ≤28                          | -    | 7    | 12   | mA    |
| <b>SHUTDOWN SECTION</b>                                       |                     |  |      |      |      |       |
| Shutdown Feedback voltage                                     | V <sub>SD</sub>     | V <sub>fb</sub> ≥6.5V                        | 6.9  | 7.5  | 8.1  | V     |
| Thermal shutdown temperature (T <sub>j</sub> ) <sup>(1)</sup> | T <sub>SD</sub>     | -  | 140  | 160  | -    | °C    |
| Over voltage protection                                       | V <sub>OVP</sub>    | V <sub>CC</sub> ≥24V                         | 25   | 27   | 29   | V     |

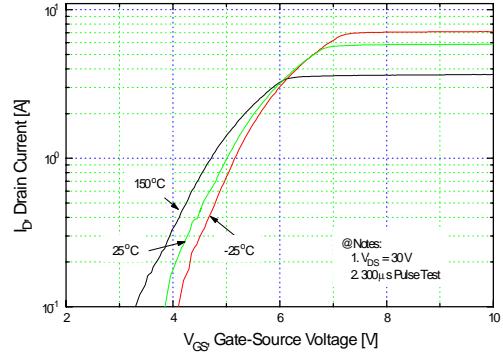
**NOTE:**

- These parameters, although guaranteed, are not 100% tested in production
- These parameters, although guaranteed, are tested in EDS(water test) process

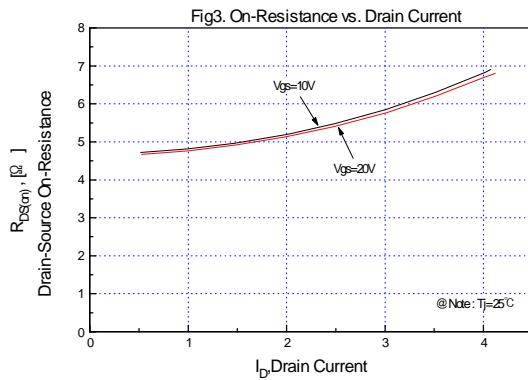
## Typical Performance Characteristics



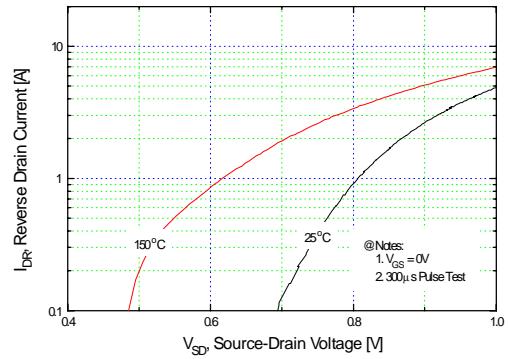
**Figure 1. Output Characteristics**



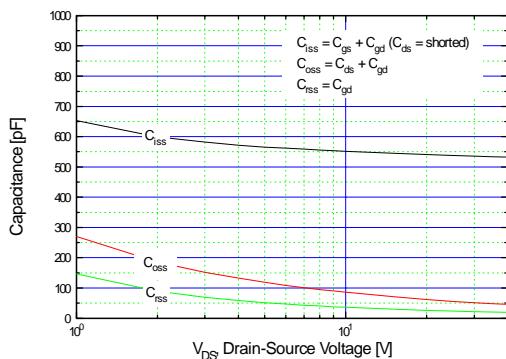
**Figure 2. Transfer Characteristics**



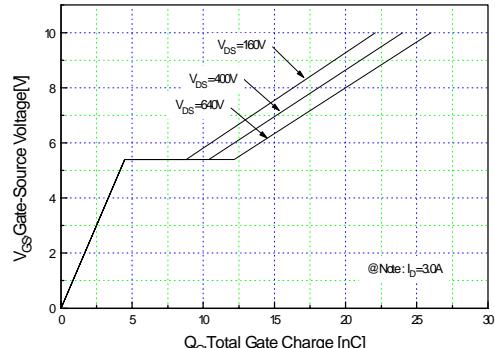
**Figure 3. On-Resistance vs. Drain Current**



**Figure 4. Source-Drain Diode Forward Voltage**

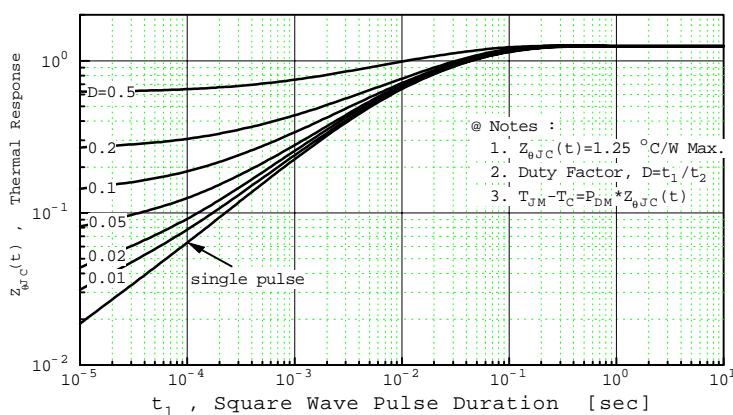
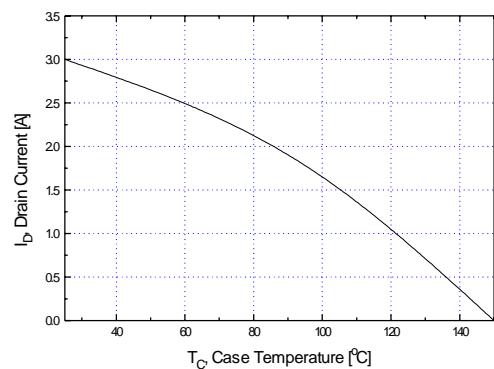
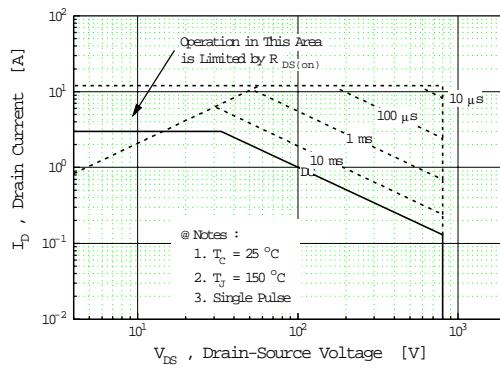
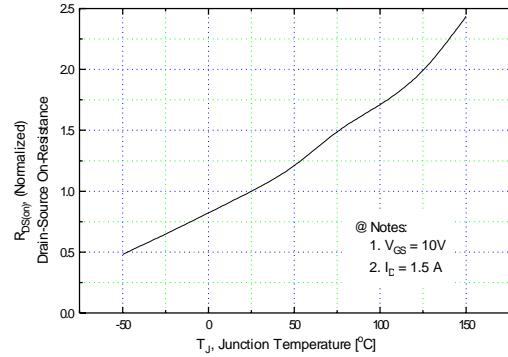
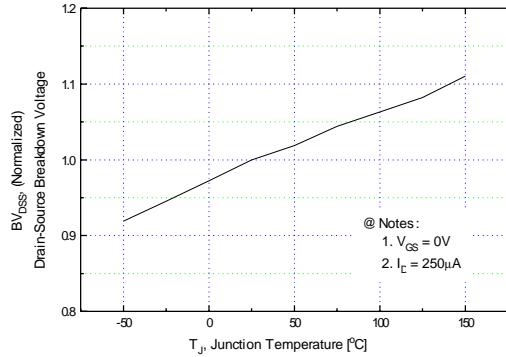


**Figure 5. Capacitance vs. Drain-Source Voltage**



**Figure 6. Gate Charge vs. Gate-Source Voltage**

## typical performance characteristics (continued)



## typical performance characteristics (control part)

(These characteristic graphs are normalized at  $T_a = 25^\circ\text{C}$ )

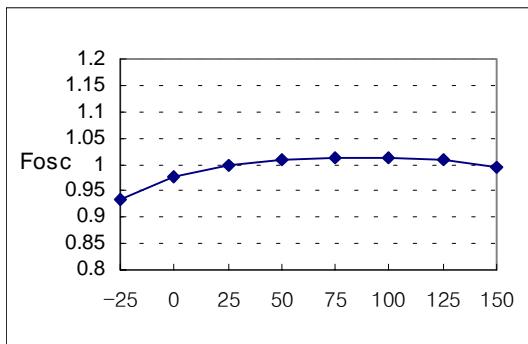


Figure 1. Operating Frequency

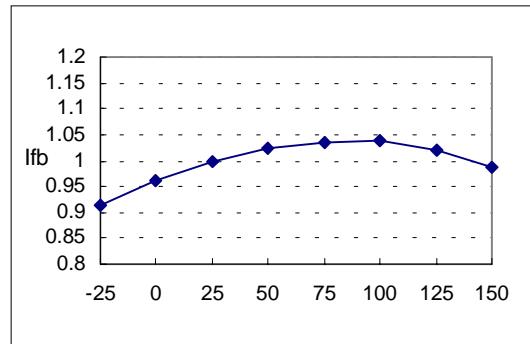


Figure 2. Feedback Source Current

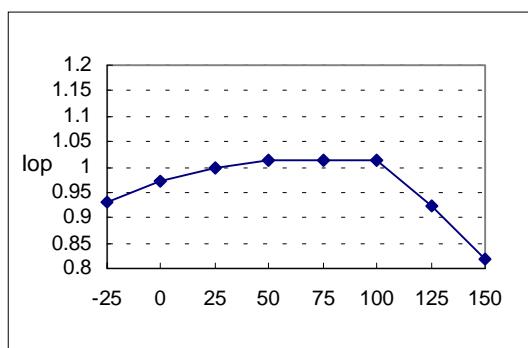


Figure 3. Operating Current

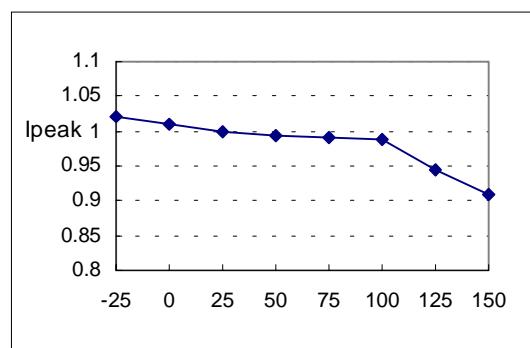


Figure 4. Max Inductor Current

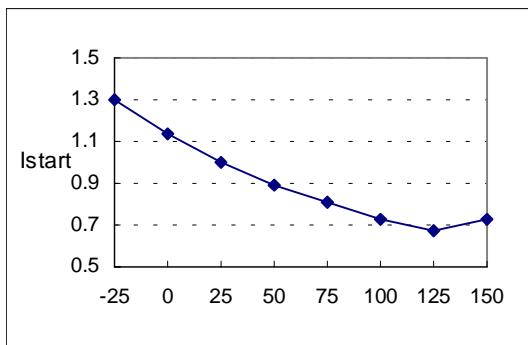


Figure 5. Start up Current

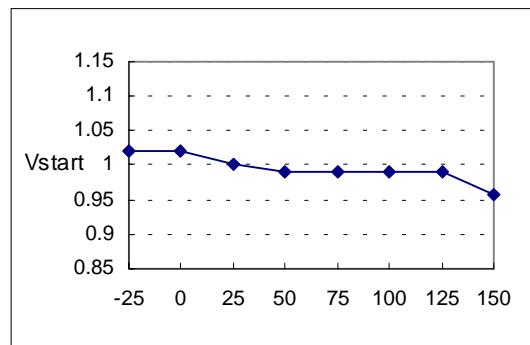


Figure 6. Start Threshold Voltage

## typical performance characteristics (continued)

(These characteristic graphs are normalized at  $T_a = 25^\circ\text{C}$ )

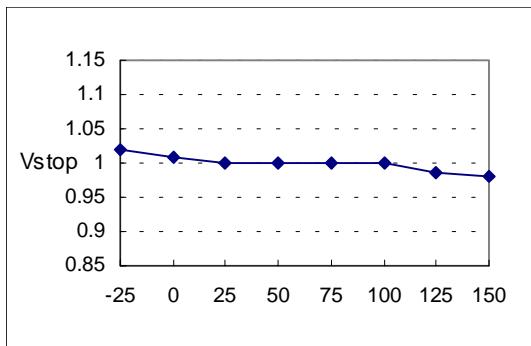


Figure 7. Stop Threshold Voltage

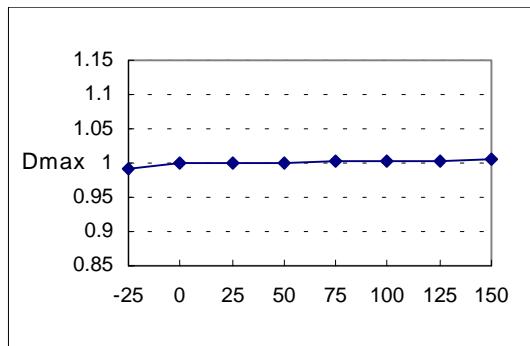


Figure 8. Maximum Duty Cycle

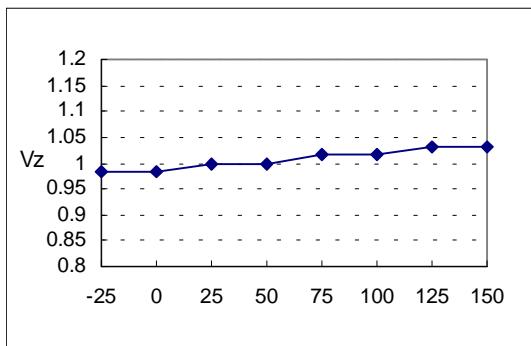


Figure 9. VCC Zener Voltage

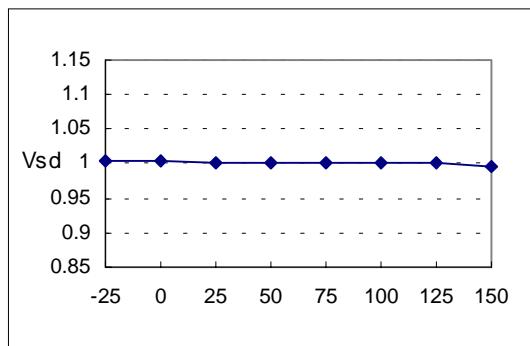


Figure 10. Shutdown Feedback Voltage

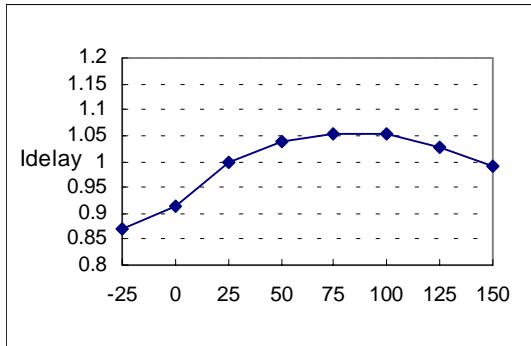


Figure 11. Shutdown Delay Current

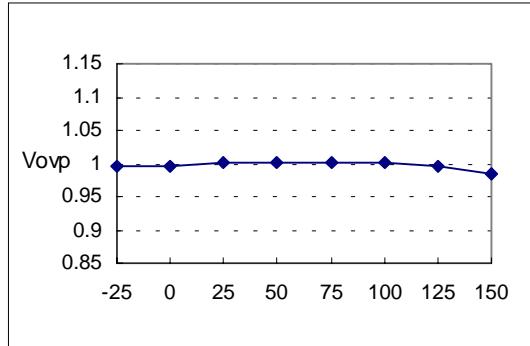


Figure 12. Over Voltage Protection

## typical performance characteristics (continued)

(These characteristic graphs are normalized at  $T_a = 25^\circ\text{C}$ )

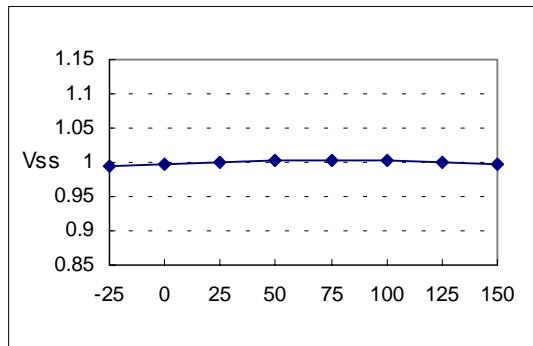


Figure13. Soft Start Voltage

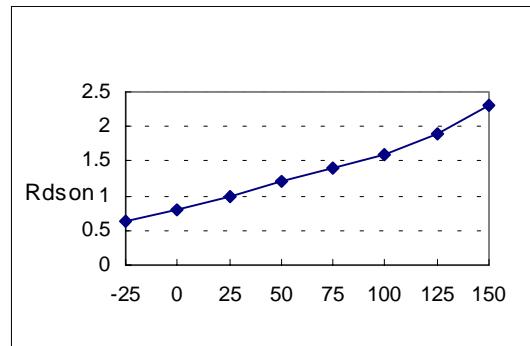
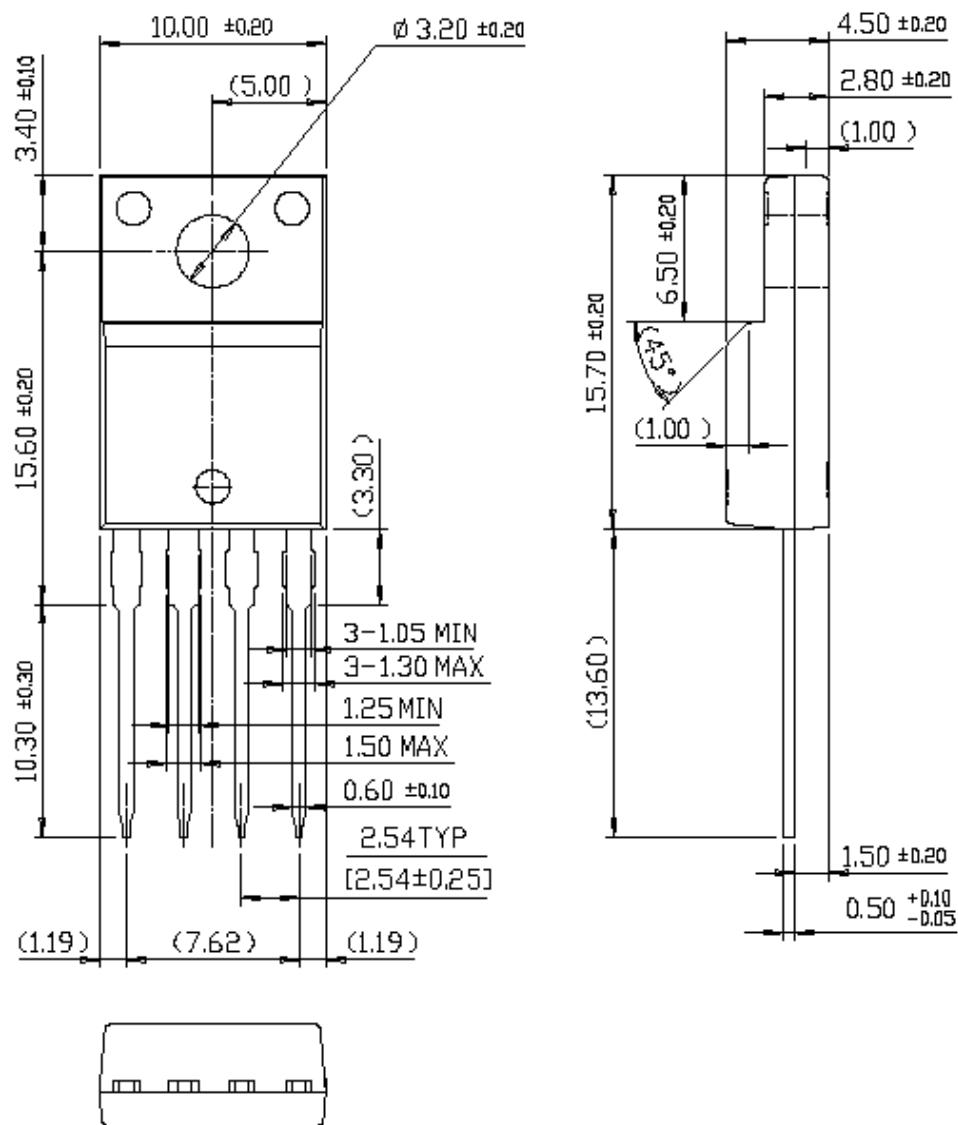


Figure 14. Drain Source Turn-on Resistance

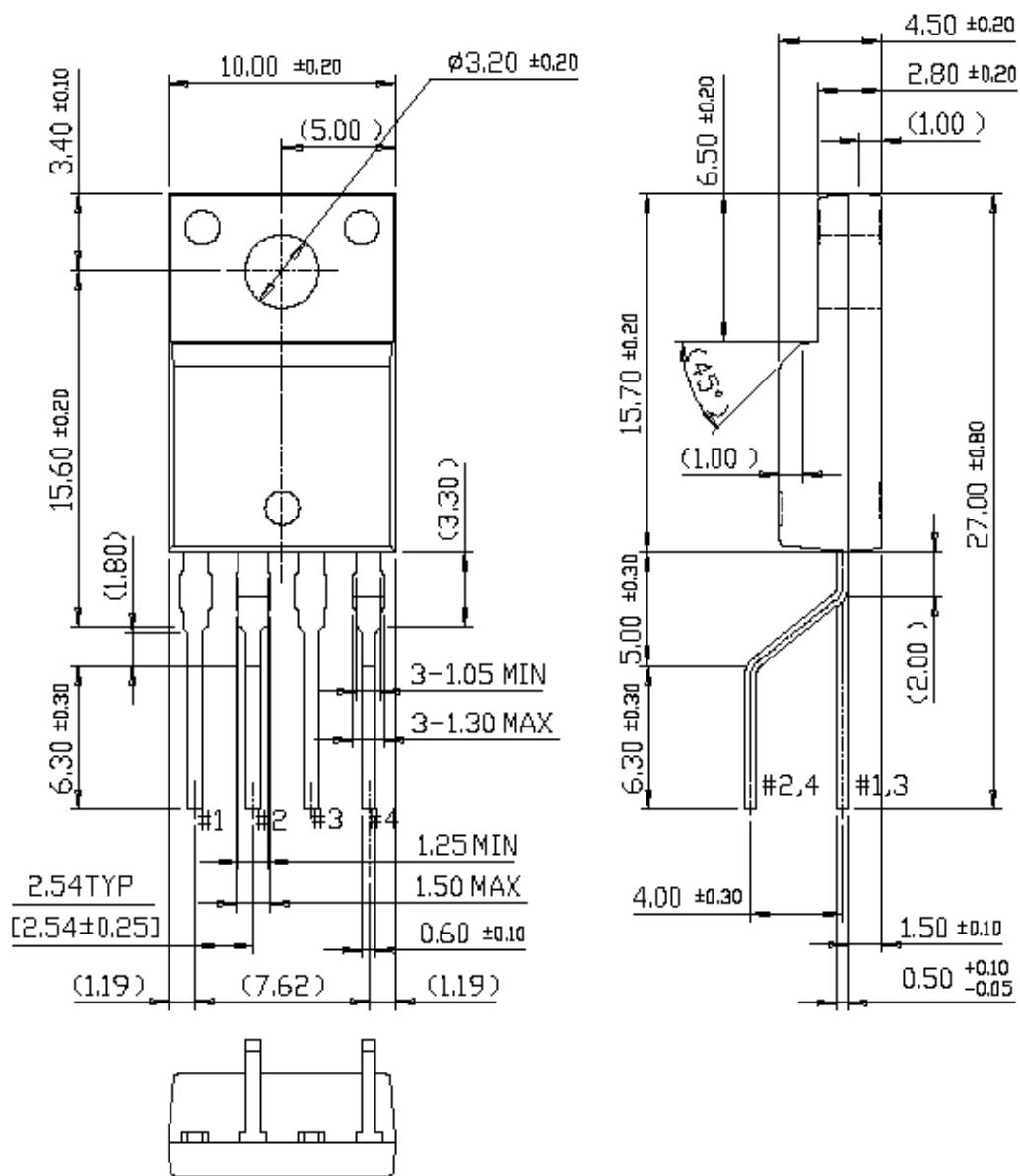
## Package Dimensions

TO-220F-4L



## Package Dimensions (Continued)

**TO-220F-4L (Forming)**



## Ordering Information

| Product Number | Package             | Rating   | Operating Temperature |
|----------------|---------------------|----------|-----------------------|
| KA5H0380R-TU   | TO-220F-4L          | 800V, 3A | -25°C to +85°C        |
| KA5H0380R-YDTU | TO-220F-4L(Forming) |          |                       |
| KA5M0380R-TU   | TO-220F-4L          | 800V, 3A | -25°C to +85°C        |
| KA5M0380R-YDTU | TO-220F-4L(Forming) |          |                       |
| KA5L0380R-TU   | TO-220F-4L          | 800V, 3A | -25°C to +85°C        |
| KA5L0380R-YDTU | TO-220F-4L(Forming) |          |                       |

TU : Non forming Type

YDTU :forming Type



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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.