

General Description

The MDF11N60 uses advanced MagnaChip's MOSFET Technology, which provides low on-state resistance, high switching performance and excellent quality.

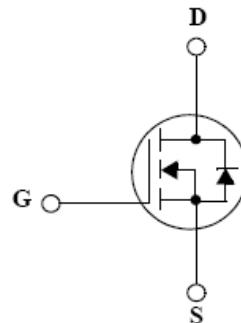
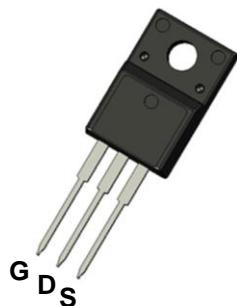
MDF11N60 is suitable device for SMPS, high Speed switching and general purpose applications.

Features

- $V_{DS} = 600V$
- $V_{DS} = 660V$
- $I_D = 11A$
- $R_{DS(ON)} \leq 0.55\Omega$
- @ T_{jmax}
- @ $V_{GS} = 10V$
- @ $V_{GS} = 10V$

Applications

- Power Supply
- PFC
- High Current, High Speed Switching



Absolute Maximum Ratings ($T_a = 25^\circ C$)

Characteristics	Symbol	Rating	Unit
Drain-Source Voltage	V_{DSS}	600	V
Drain-Source Voltage @ T_{jmax}	$V_{DSS} @ T_{jmax}$	660	V
Gate-Source Voltage	V_{GSS}	± 30	V
Continuous Drain Current (*)	I_D	11	A
		6.9	A
Pulsed Drain Current ⁽¹⁾	I_{DM}	44	A
Power Dissipation	P_D	49	W
		0.39	$W/^\circ C$
Peak Diode Recovery $dv/dt^{(3)}$	dv/dt	4.5	V/ns
Single Pulse Avalanche Energy ⁽⁴⁾	E_{AS}	720	mJ
Junction and Storage Temperature Range	T_J, T_{stg}	-55~150	$^\circ C$

* Id limited by maximum junction temperature

Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient ⁽¹⁾	R_{BJA}	62.5	$^\circ C/W$
Thermal Resistance, Junction-to-Case ⁽¹⁾	R_{BJC}	2.55	

Ordering Information

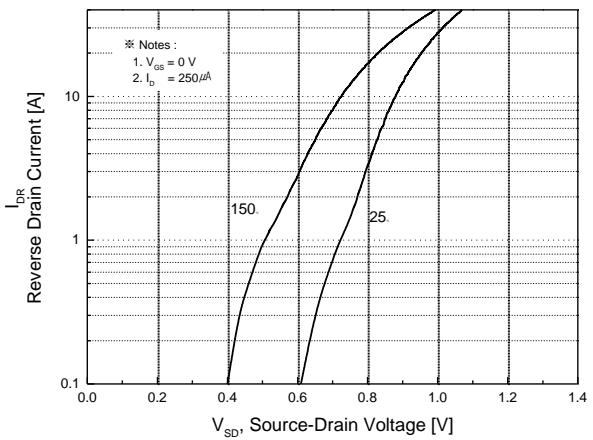
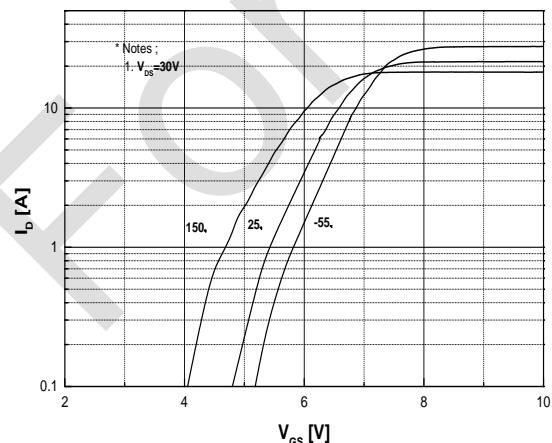
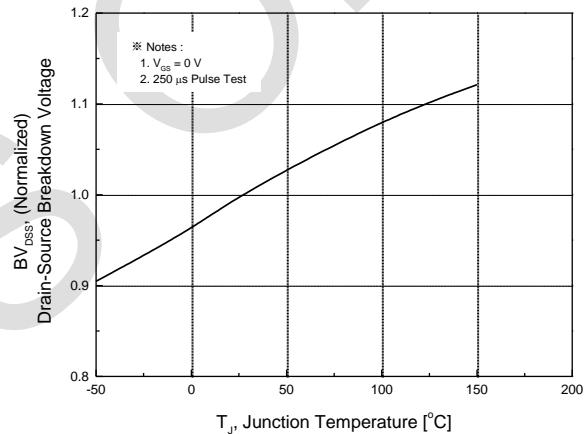
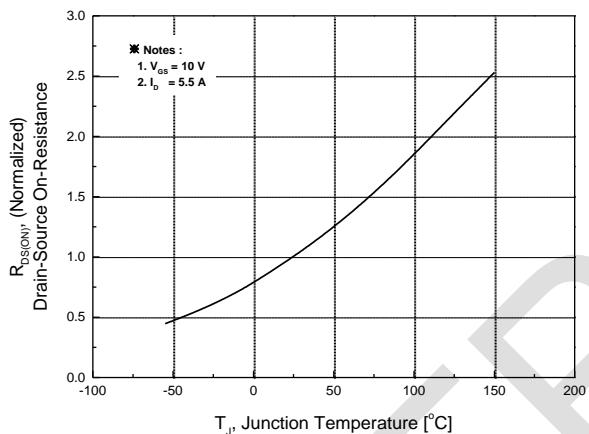
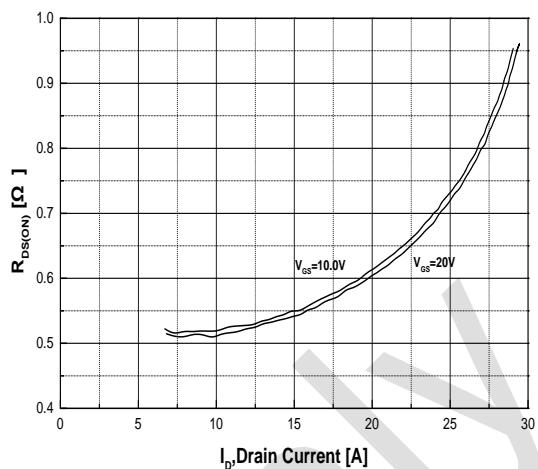
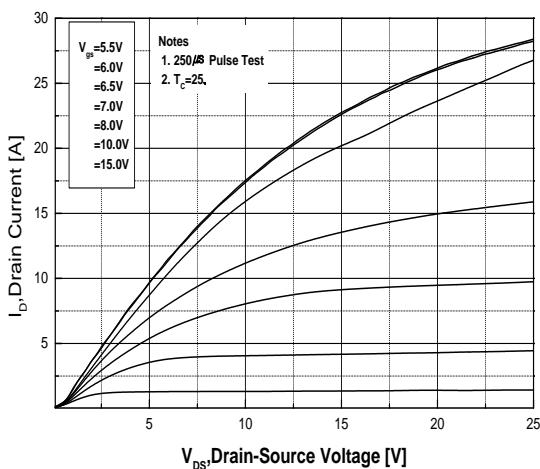
Part Number	Temp. Range	Package	Packing	RoHS Status
MDF11N60TH	-55~150°C	TO-220F	Tube	Halogen Free

Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D = 250μA, V _{GS} = 0V	600	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	3.0	-	5.0	
Drain Cut-Off Current	I _{DSS}	V _{DS} = 600V, V _{GS} = 0V	-	-	1	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±30V, V _{DS} = 0V	-	-	100	nA
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D = 5.5A	-	0.45	0.55	Ω
Forward Transconductance	g _{fs}	V _{DS} = 30V, I _D = 5.5A	-	13	-	S
Dynamic Characteristics						
Total Gate Charge	Q _g	V _{DS} = 480V, I _D = 11A, V _{GS} = 10V ⁽³⁾	-	38.4	-	nC
Gate-Source Charge	Q _{gs}		-	11.2	-	
Gate-Drain Charge	Q _{gd}		-	14	-	
Input Capacitance	C _{iss}	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	-	1700	-	pF
Reverse Transfer Capacitance	C _{rss}		-	6.2	-	
Output Capacitance	C _{oss}		-	184	-	
Turn-On Delay Time	t _{d(on)}	V _{GS} = 10V, V _{DS} = 300V, I _D = 11A, R _G = 25Ω ⁽³⁾	-	38	-	ns
Rise Time	t _r		-	50	-	
Turn-Off Delay Time	t _{d(off)}		-	76	-	
Fall Time	t _f		-	33	-	
Drain-Source Body Diode Characteristics						
Maximum Continuous Drain to Source Diode Forward Current	I _S	I _F = 11A, dI/dt = 100A/μs ⁽³⁾	-	11	-	A
Source-Drain Diode Forward Voltage	V _{SD}		-	-	1.4	V
Body Diode Reverse Recovery Time	t _{rr}		-	430	-	ns
Body Diode Reverse Recovery Charge	Q _{rr}		-	4.0	-	μC

Note :

1. Pulse width is based on R_{θJC} & R_{θJA} and the maximum allowed junction temperature of 150°C.
2. Pulse test: pulse width ≤300us, duty cycle≤2%, pulse width limited by junction temperature T_{J(MAX)}=150°C.
3. I_{SD} ≤11A, di/dt≤200A/us, V_{DD}=50V, R_g =25Ω, Starting T_J=25°C
4. L=10.9mH, I_{AS}=11A, V_{DD}=50V, R_g =25Ω, Starting T_J=25°C



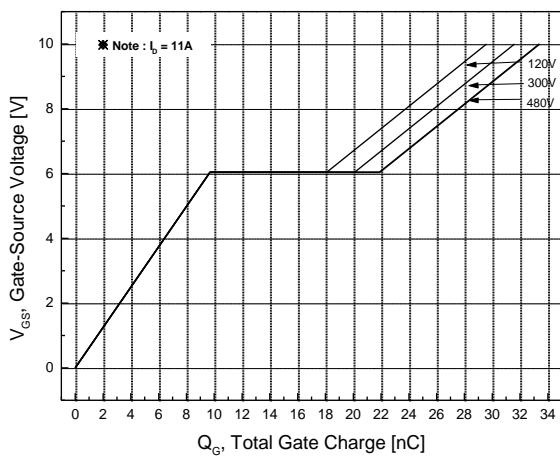


Fig.7 Gate Charge Characteristics

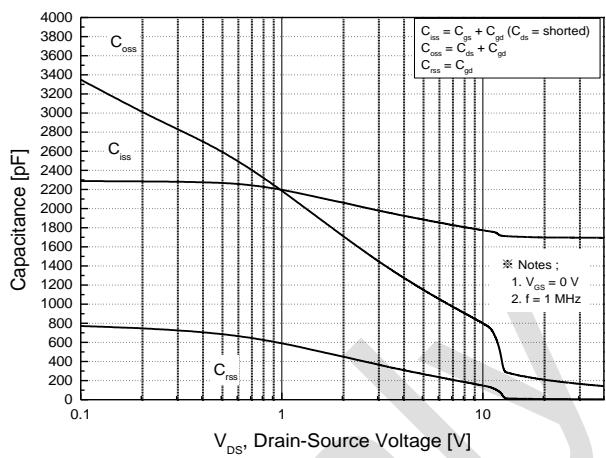


Fig.8 Capacitance Characteristics

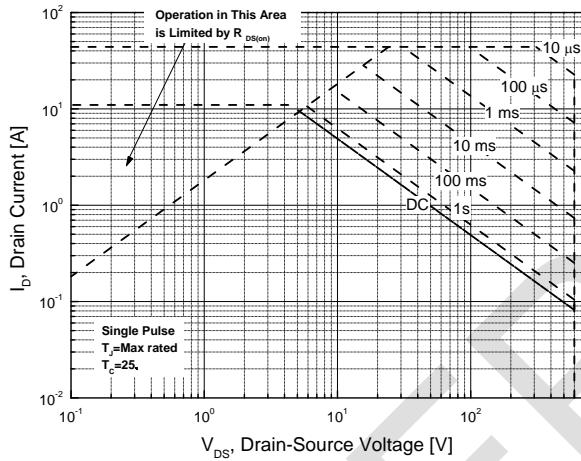


Fig.9 Maximum Safe Operating Area

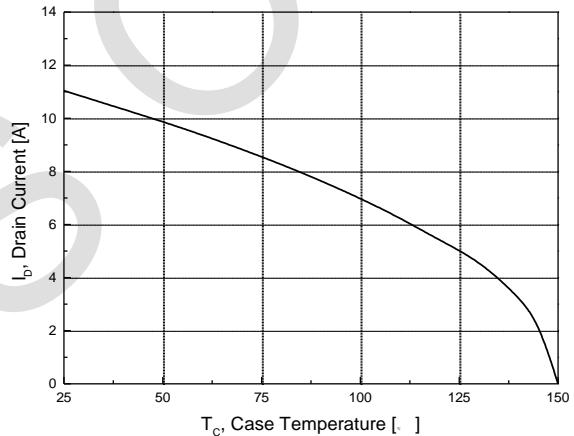


Fig.10 Maximum Drain Current vs. Case Temperature

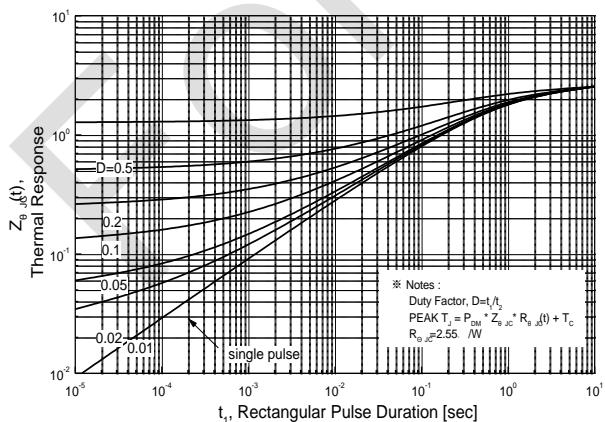


Fig.11 Transient Thermal Response Curve

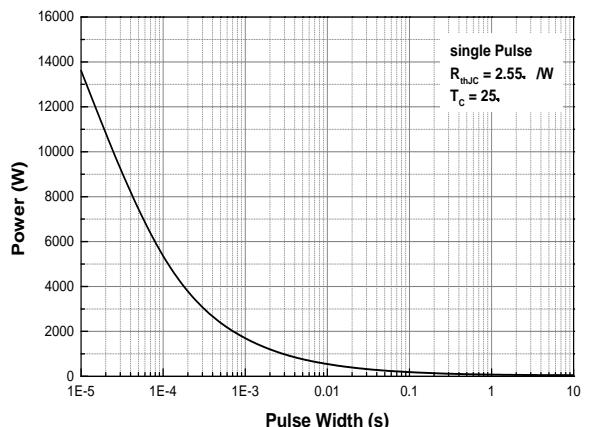
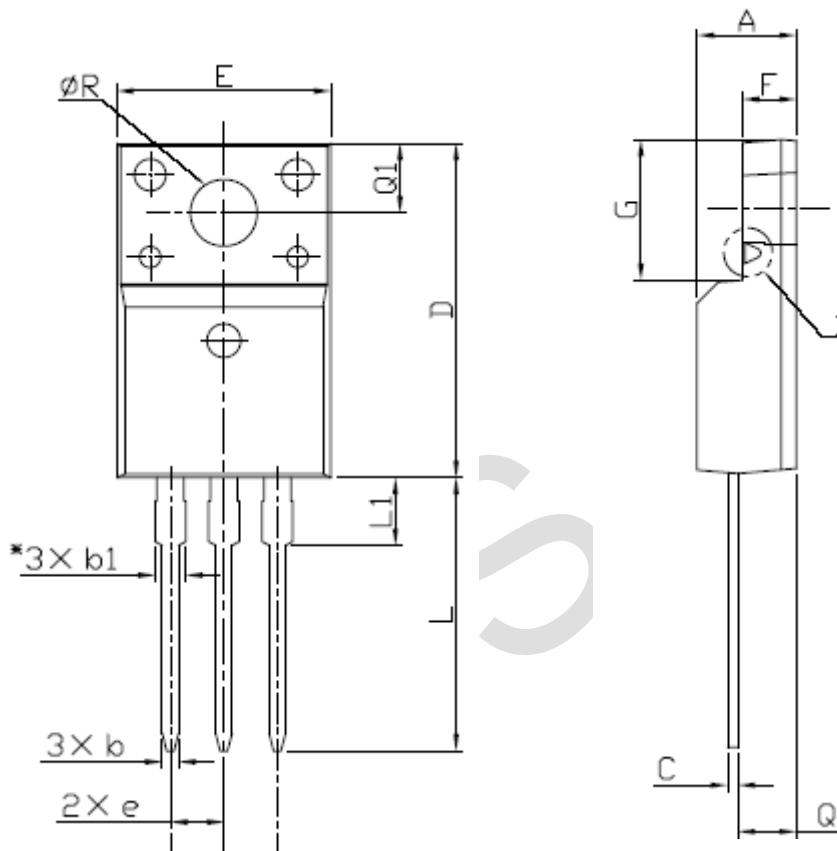


Fig.12 Single Pulse Maximum Power Dissipation

Physical Dimensions

3 Leads, TO-220F

Dimensions are in millimeters unless otherwise specified



Symbol	Min	Nom	Max
A	4.50		4.93
b	0.63		0.91
b1	1.15		1.47
C	0.33		0.63
D	15.47		16.13
E	9.60		10.71
e		2.54	
F	2.34		2.84
G	6.48		6.90
L	12.24		13.72
L1	2.79		3.67
Q	2.52		2.96
Q1	3.10		3.50
ØR	3.00		3.55

For EPS Only

DISCLAIMER:

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