

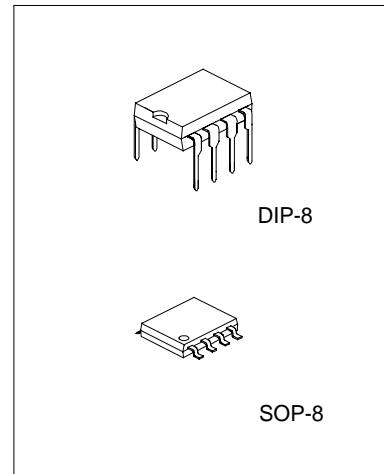
## DUAL OPERATIONAL AMPLIFIER

### DESCRIPTION

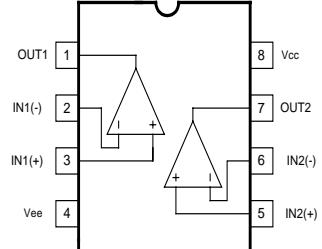
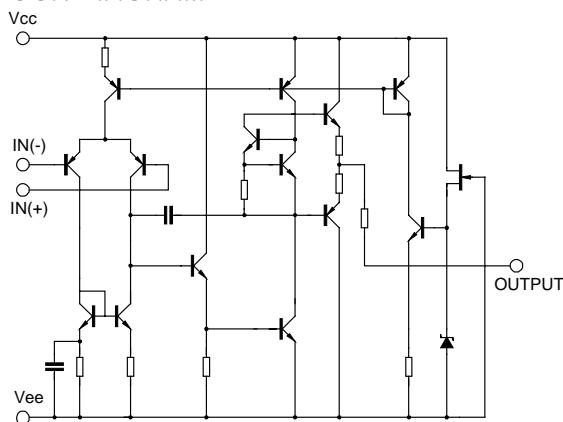
The UTC4558 is a monolithic integrated circuit designed for dual operational amplifier.

### FEATURES

- \*No frequency compensation required.
- \*No latch-up
- \*Large common mode and differential voltage range
- \*Parameter tracking over temperature range
- \*Gain and phase match between amplifiers
- \*Internally frequency compensated
- \*Low noise input transistors



### BLOCK DIAGRAM



### ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Value	Unit
Supply Voltage	Vcc	$\pm 22$	V
Differential input voltage	$V_{I(DIFF)}$	$\pm 18$	V
Power Dissipation	Pd	400	mW
Input Voltage	$V_I$	$\pm 15$	V
Operating Temperature	$T_{OPR}$	$0\text{--}+70$	$^{\circ}\text{C}$
Storage Temperature	$T_{STG}$	$-65\text{--}+150$	$^{\circ}\text{C}$

**ELECTRICAL CHARACTERISTICS( Ta=25°C ,Vcc=15V,Vee=-15V)**

Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Supply Current	Icc			3.5	5.6	mA
Input offset voltage	V <sub>IO</sub>	R <sub>S</sub> <10kΩ		2	6	mV
Input offset current	I <sub>IO</sub>			5	200	nA
Input bias current	I <sub>BIAS</sub>			30	500	nA
Large signal voltage gain	G <sub>V</sub>	V <sub>O</sub> (p-p)=10V,R <sub>L</sub> <2kΩ	20	200		V/mV
Common Mode Input Voltage Range	V <sub>I(R)</sub>		±12	±13		V
Common Mode Rejection Ratio	CMRR	R <sub>S</sub> <10kΩ	70	90		dB
Supply Voltage Rejection Ratio	PSRR	R <sub>S</sub> <10kΩ	76	90		dB
Output Voltage swing	V <sub>O</sub> (p-p)	R <sub>L</sub> >10kΩ		±12	±14	V
Power Consumption	P <sub>C</sub>			70	170	mV
Slew Rate	SR	V <sub>i</sub> =10V,R <sub>L</sub> >2kΩ,CL<100pF	1.2			V/μs
Rise Time	T <sub>TRIS</sub>	V <sub>i</sub> =20mV,R <sub>L</sub> >2kΩ,CL<100pF		0.3		μs
Overshoot	OS	V <sub>i</sub> =20mV,R <sub>L</sub> >2kΩ,CL<100pF		15		%

**TYPICAL PERFORMANCE CHARACTERISTICS**

Fig.1 Positive output voltage swing vs Load resistance

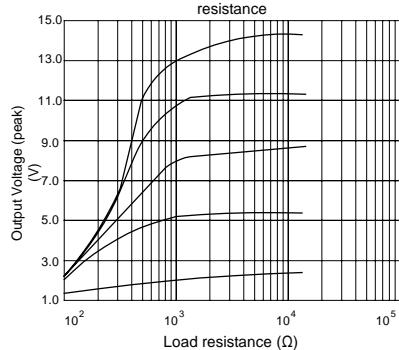


Fig.2 Positive output voltage swing vs Load resistance

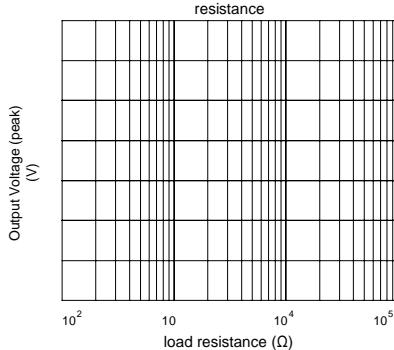


Fig.3 Power bandwith(large signal swing vs frequency)

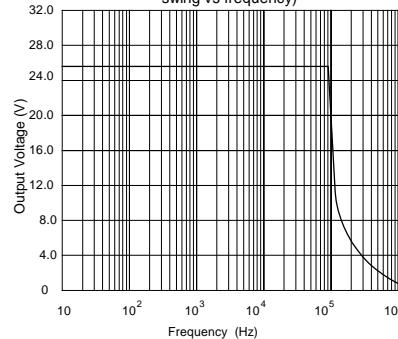


Fig. 4 Burst Noise vs Rs

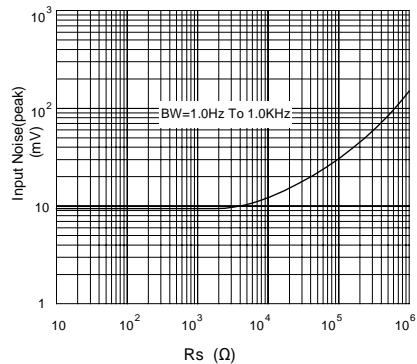


Fig. 6 Output Noise vs Rs

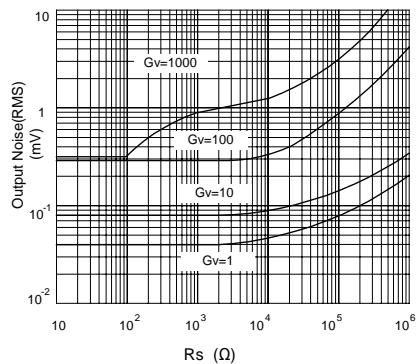


Fig. 8 Open loop frequency response

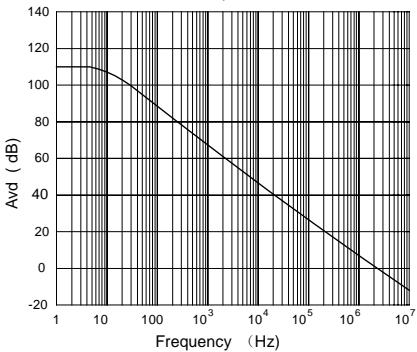


Fig. 5 RMS Noise vs Rs

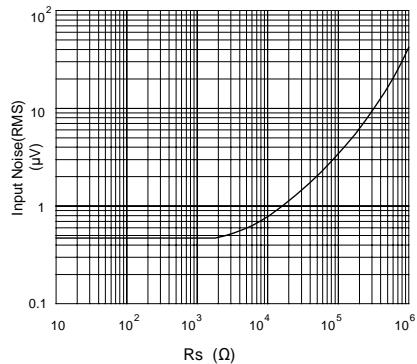


Fig. 7 Spectral Noise Density

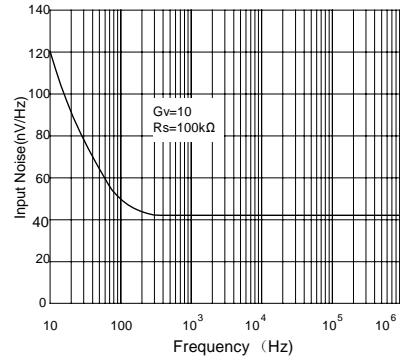
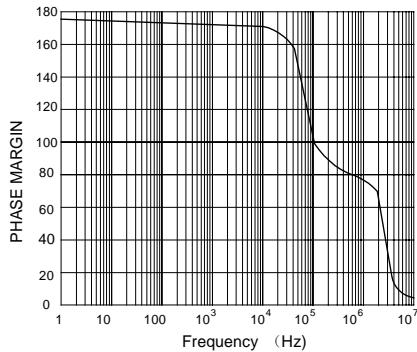
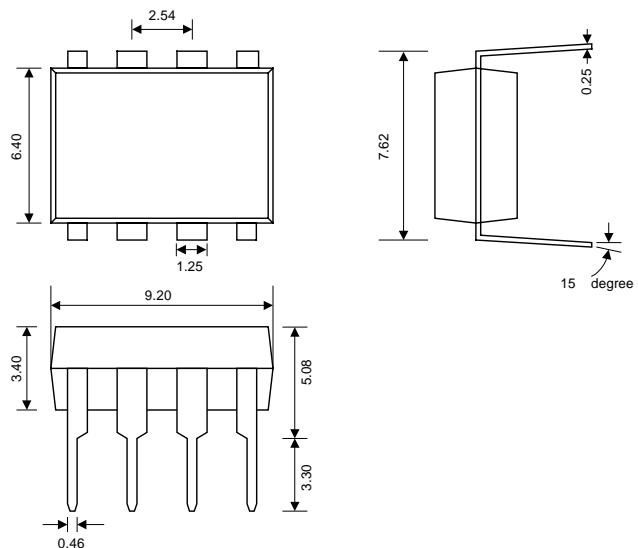


Fig. 9 PHASE MARGIN vs FREQUENCY



## PACKAGE DIMENSIONS

8-DIP-P-300



8-SOP-P

